

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

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|--|---|----------------|---------------|-------------|----------------|--|---------------|-------|
| Title of Invention | [METHOD OF FABRICATING THIN FILM TRANSISTOR ARRAY SUBSTRATE AND STACKED THIN FILM STRUCTURE] | | | | | | | |
| Application Number : | | | | | | | | |
| Date : | | | | | | | | |
| First Named Applicant: | Mr. Han-Chung Lai | | | | | | | |
| Confirmation Number: | | | | | | | | |
| Attorney Docket Number: | 12406-US-PA | | | | | | | |
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| <table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Dr. Belinda Lee Registered Number: 46,863</td><td>/Belinda Lee/</td><td>Agent</td></tr></table> | | | Submitted By: | Elec. Sign. | Sign. Capacity | Dr. Belinda Lee Registered Number: 46,863 | /Belinda Lee/ | Agent |
| Submitted By: | Elec. Sign. | Sign. Capacity | | | | | | |
| Dr. Belinda Lee Registered Number: 46,863 | /Belinda Lee/ | Agent | | | | | | |

| Documents being submitted: | Files |
|----------------------------|--|
| us-assignment | 12406-usassn.xml us-assignment.xsl us-assignment.dtd 12406assignment01.tif 12406assignment02.tif |
| us-request | 12406-usrequ.xml us-request.dtd us-request.xsl |
| us-fee-sheet | 12406-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd |
| us-declaration | 12406declaration01.tif |
| us-declaration | 12406declaration02.tif |
| application-body | 12406-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd 12406us0001.tif 12406us0002.tif 12406us0003.tif 12406us0004.tif 12406us0005.tif 12406us0006.tif 12406us0007.tif 12406us0008.tif |

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Comments

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